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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Eliyahou Harari

Title:

Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element

Application No.: 10/791,486

Filing Date: March 1, 2004

Examiner: Nguyen, Viet Q.

Group Art Unit: 2827

Docket No.: SNDK.304US1

Conf. No.: 1878

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 27, 2005

Mary E. Buggie

Mary E. Buggie

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION AND AMENDMENT

Sir:

This is in response to the non-final Office Action dated July 25, 2005.

Claim Amendments are reflected in the listing of claims, which begins on page 2 of this paper.

Specification Amendments begin on page 9 of this paper.

Remarks begin on page 10 of this paper.

Reconsideration is kindly requested in light of the following amendments and remarks.

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